

IN THE UNITED STATES PATENT OFFICE

APPLICANT: Martin Seifert

Entry of U.S. National Stage of
International application PCT/EP99/07531

For: MULTIPLEXER CIRCUIT AND ANALOGUE-TO-DIGITAL CONVERTER

PRELIMINARY AMENDMENT

Please amend the above application as follows.

IN THE CLAIMS:

Please cancel claims 1-18 and insert new claims 19-36 as follows:

Sub B2
19. A multiplexer circuit comprising at least two input channels and an output channel, each input channel comprising a first transmission gate which can be switched on by a select signal for connecting the input channel to the output channel, at least one of the input channels comprising a bypass circuit for preventing a current flowing through the first transmission gate from reaching the other input channel, and a second transmission gate, said multiplexer circuit further comprising a control circuit for controlling said bypass circuit.

20. The multiplexer circuit according to claim 19, wherein said control circuit controls said bypass circuit dependent upon a voltage in the input channel.

21. The multiplexer circuit according to claim 19, wherein said control circuit comprises a sense circuit to control said bypass circuit by sensing a voltage in the input channel.